

AMENDMENTS TO THE SPECIFICATION

Please amend the specification as follows:

Please replace the paragraph beginning at page 6, line 4 with the following replacement paragraph:

Although the design of transformer structures suitable to a given electrical assembly may vary depending on the characteristics or design of a particular electrical assembly, one advantageous configuration is particularly suitable for definition on a planar electrical assembly (e.g., on a board, card, package, or integrated circuit chip). In such configurations, a voltage plane (e.g., voltage plane **102**) is generally employed to reduce inductive coupling between traces **10**, **20**, **30**, **40**, and **50** on the electrical assembly. In one realization, apertures **112**, **112A**, **112B**, **112C**, **112D** are defined in the voltage plane to provide the integrated transformer structures **110**, **110A**, **110B**, **110C**, and **110D**. By orienting corresponding ones of the electrical traces (e.g., electrical traces **121**, **122**, **123**, etc.) to traverse the apertures such that the relative direction of current flows therethrough opposes that through neighboring ones of the electrical connections, compensating and opposing crosstalk signals may be induced at the integrated transformer structures to provide crosstalk cancellation. In some configurations, electrical traces (e.g., **121**, **122**, **123**, etc.) may be defined over (or under) an opening in the voltage plane below (or above). In other configurations, the electrical traces may be defined coplanar with the voltage plane and simply traverse in apertures defined therein. In such configurations, electrical traces and voltage planes may be formed using any suitable conventional process. Materials (such as metals, doped poly-silicon, etc.) suitable for a particular electrical assembly will be appreciated by persons of ordinary skill in the art.